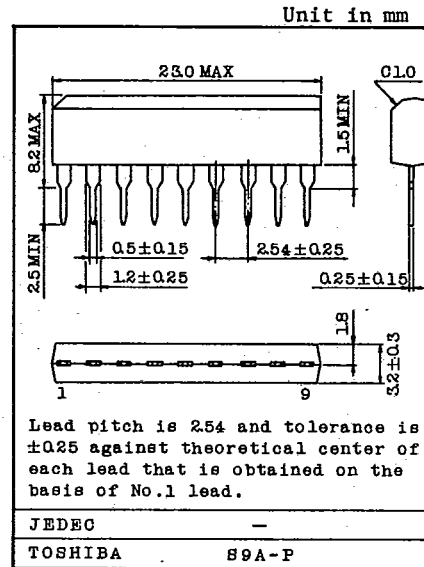


TA7325P

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DUAL PRE AMPLIFIER

- Dual Pre Amplifier for Car or Home Stereo Use.
- High Voltage Gain :
G_{VO}=100dB (Typ.) at f=1kHz
- Excellent Channel Separation and High Ripple Rejection : CH. Sep.=70dB (Typ.)
R.R.=50dB (Typ.)
- Low Noise : V_{N1}=1.0μV_{rms} (Typ.)
at R_g=2.2kΩ, BW=15Hz~30kHz
- Build in Muting Circuit : 2V ≥ V₉ ≥ 0.9V
- Wide Operating Supply Voltage Range : V_{CC}=7~18V.



MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	18	V
Power Dissipation (Note)	P _D	700	mW
Operating Temperature	T _{opr}	-25 ~ 75	°C
Storage Temperature	T _{stg}	-55 ~ 150	°C

Note : Derated above Ta=25°C in the proportion of 5.6mW/°C.

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, V_{CC}=10V, f=1kHz, R_g=600Ω, R_L=10kΩ, Ta=25°C)

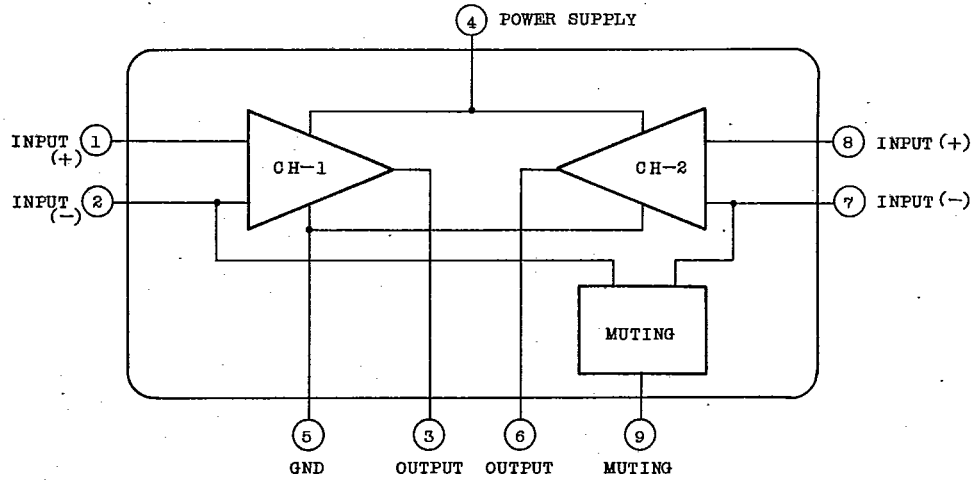
CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	I _{CC}	1	V _{IN} =0	-	5.5	8.5	mA
Voltage Gain	G _{VO}	1	C _f =100μF, R _f =0	-	100	-	dB
Maximum Output Voltage	V _{OM}	2	THD=0.5%, NAB EQ	2.0	2.8	-	V _{rms}
Equivalent Input Noise Voltage	V _{N1}	2	R _g =2.2kΩ, BPF=15Hz~30kHz	-	1.0	2.5	μV _{rms}
Input Resistance	R _{IN}	2	V _O =1V _{rms}	-	100	-	kΩ
Channel Separation	CHsep	2	f=10kHz, R _g =2.2kΩ, V _O =1V _{rms}	-	70	-	dB
Ripple Rejection	R.R	2	f=100Hz, V _{IN} =1V _{rms} , CB=NO connection	-	50	-	dB
Muting Ratio	M.R	2	V ₉ =1V, 0dB=1V _{rms}	-	80	-	dB

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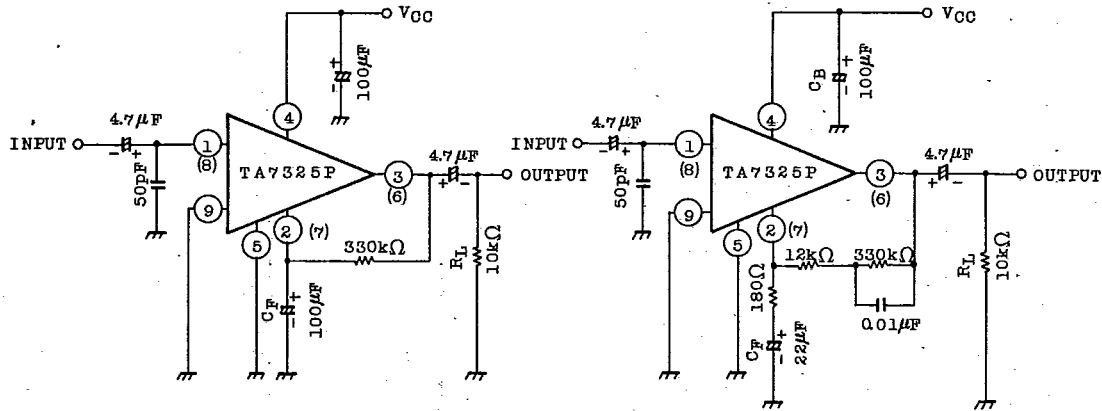
EQUIVALENT BLOCK DIAGRAM



TEST CIRCUIT

1. G_{VO} , I_{CC}

2. V_{OM} , V_{NI} , R_{IN} , R_{R}



TOSHIBA

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APPLICATION

(1) DECIDE OF FEEDBACK RESISTANCE

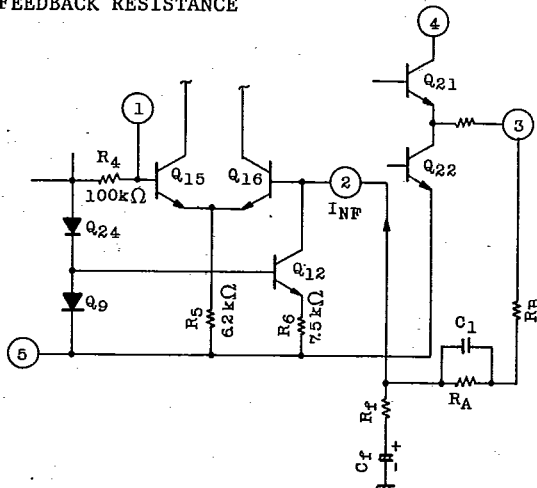


Fig.1

Fig.1 shows the internal circuit.

The optimum D.C output voltage is decided by following equation.

$$V_3 = \frac{1}{2}V_{CC} = V_2 + I_{NF}(R_A + R_B)$$

$$I_{NF} = 10\mu A$$

$$V_3 = \frac{1}{2}V_{CC}$$

$$= V_2 + I_{NF}(R_A + R_B) \quad (V)$$

$$V_2 = V_{BE}(Q_9) + V_{BE}(Q_{24}) = 2V_{BE} \approx V_1$$

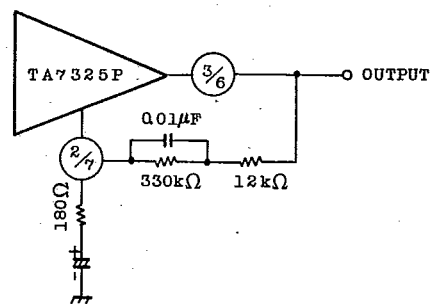
$$V_3 = \frac{1}{2}V_{CC} = 5 = 2V_{BE} + I_{NF}(R_A + R_B)$$

$$= 1.4 + 10 \times 10^{-6}(R_A + R_B)$$

$$R_A + R_B = \frac{5 - 1.4}{10 \times 10^{-6}}$$

$$= 3.6 \times 10^5$$

$$= 360 \text{ (k}\Omega\text{)}$$



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(2) MUTING CIRCUIT APPLICATION

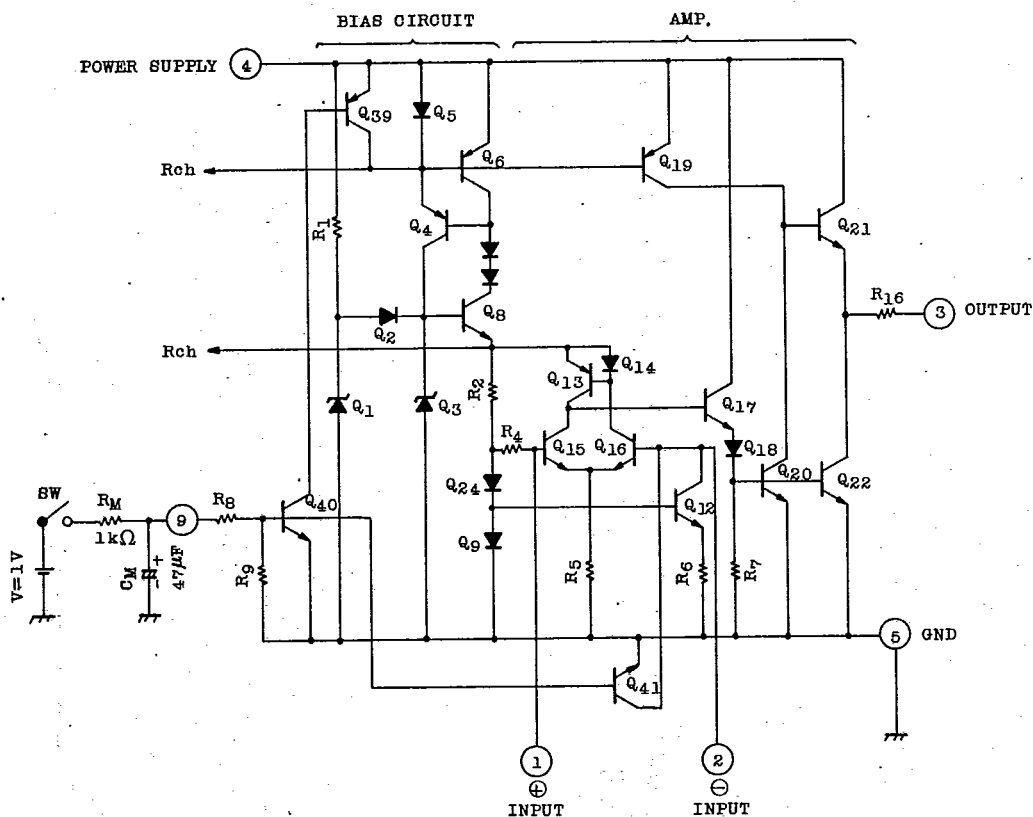


Fig.2

Fig.2 shows the equivalent circuit of (L ch) TA7325P.

The Q40 is turned ON when the voltage above 0.9V feed into PIN 9, Q41 and Q39 'turned ON' consequently and the muting operation obtained.

(3) NOTE

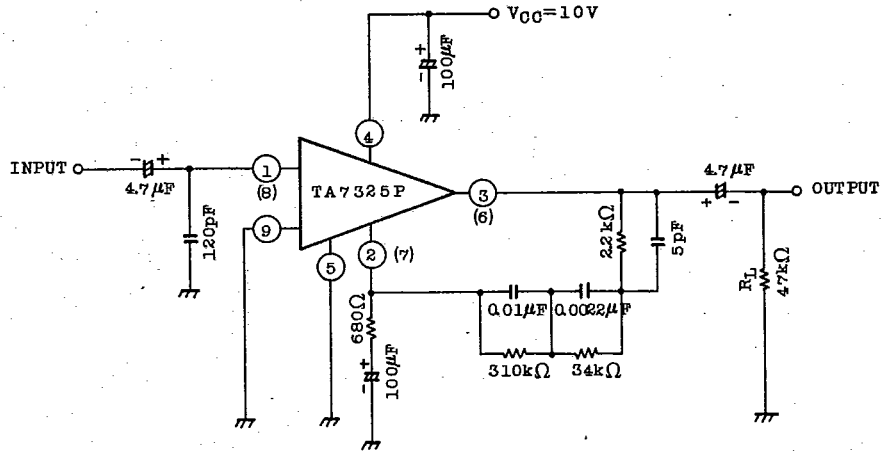
- Care should be taken not to decrease a closed loop gain less 20dB cause parasitic oscillation.
- The maximum allowable input voltage is 300 mVrms not to increase the input voltage above this value for stable operation.

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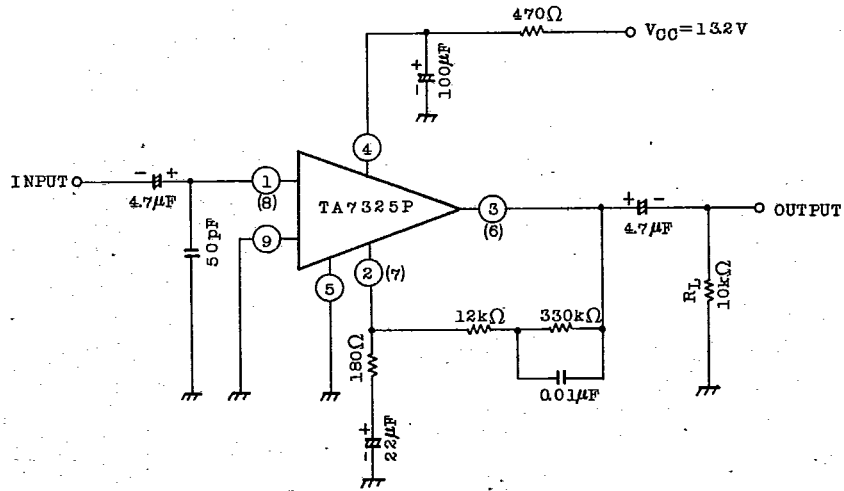
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APPLICATION

1. RIAA EQ



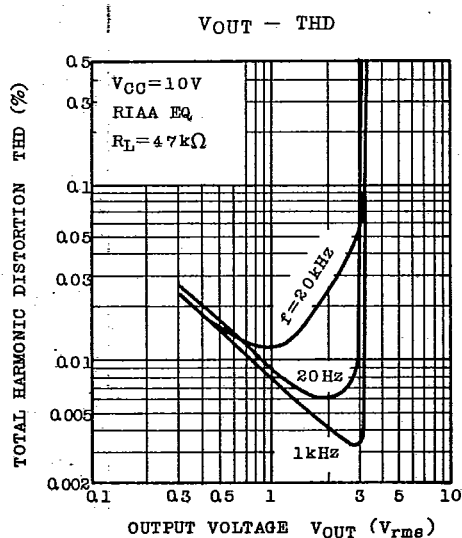
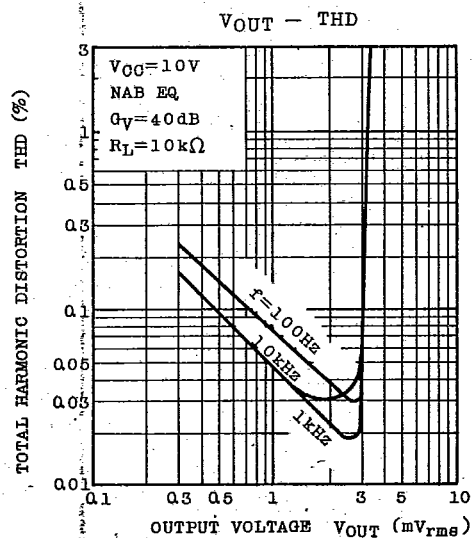
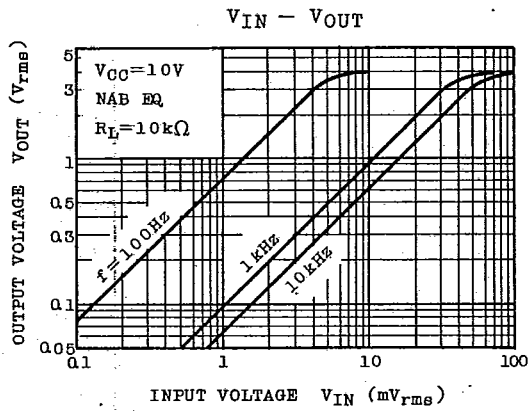
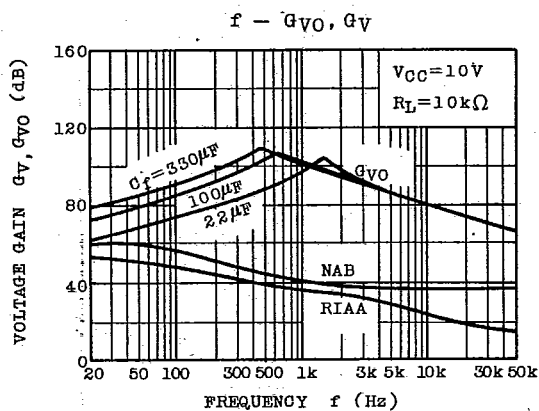
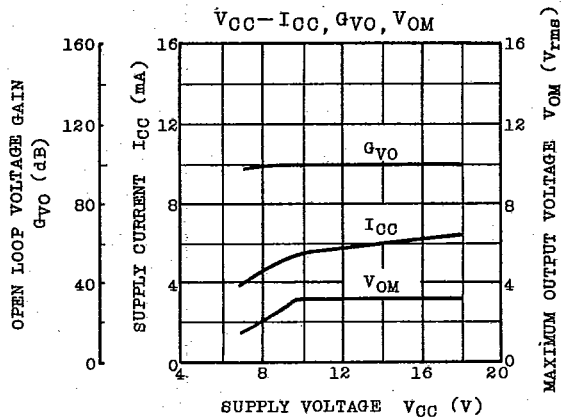
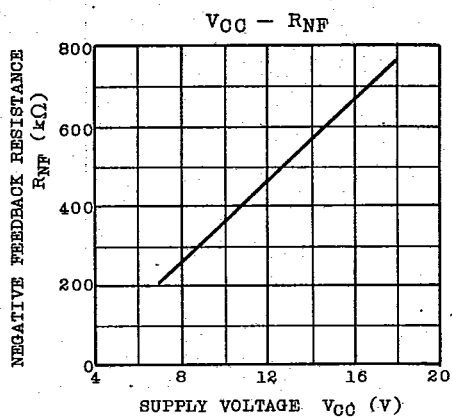
2. NAB EQ



AUDIO LINEAR IC

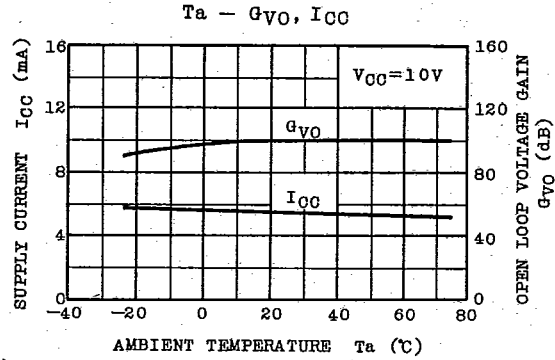
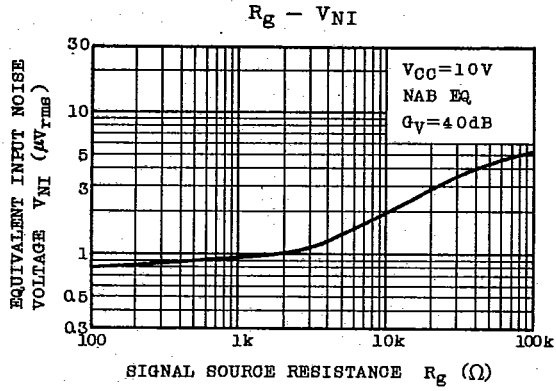
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R.R. - RIPPLE LEVEL

